



ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>MR FA</p> <p>SCANNED  Q.A. </p>	<p>O.I.P.E.</p> <p>PATENT DATE</p>
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LO<sup>3</sup> TM

APPLICATION NO. 09/974668	CONT/PRIOR F	CLASS <del>438</del>	SUBCLASS <del>101</del>	ART UNIT <del>2812</del> 2834	EXAMINER J. J. Jones <del>DAVENS</del>
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## APPLICANTS

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### **三、**

High-frequency circuit board unit, high frequency module using the same unit, electronic apparatus using the same module, and manufacturing method for the high-frequency circuit board unit

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION.											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
	_____ (Legal Instruments Examiner) _____ (Date)				

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